

### REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated December 20, 2007, has been received and its contents carefully reviewed. Applicants appreciate the indication by the Examiner that claims 10 and 17 have been allowed.

Claims 1, 2, 4-9, 11-16 and 18 are rejected by the Examiner. With this response, claims 1, 12 and 15 are amended and claims 5-9, 11 and 14 are canceled without prejudice or disclaimer. Accordingly, claims 1-4, 10, 12-13 and 15-18 are currently pending. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, claims 1, 2, 4, 5, 7, 11, 12, 14, 16 and 18 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Publication No. 2002/0039089 to Lim (hereinafter "Lim") in view of U.S. Publication No. 2001/0022572 to Murade (hereinafter "Murade"). Claims 6, 8, 13 and 15 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Lim in view of Murade, and further in view of U.S. Patent No. 5, 966, 115 to Aoki (hereinafter "Aoki").

The rejection of claims 1, 2, 4, 5, 7, 9, 11, 12, 14, 16, and 18 under 35 U.S.C. § 103(a) as being unpatentable over Lim in view of Murade is respectfully traversed and reconsideration is requested. Claims 5, 7, 9 and 14 have been cancelled without prejudice or disclaimer. Thus, the rejection, as applied to these cancelled claims, is rendered moot.

Claim 1 is allowable at least in that this claim recites a combination of elements, including, for example, "a first control signal line on the gate tape carrier packages, the first control signal line being connected to the conductive lines to supply a first control signal to the gate driving integrated circuits mounted on the gate tape carrier packages so that the gate lines of the image display part may be sequentially driven from the first one to the last one by applying the first control signal to the first one through a first signal path having the conductive lines and the first control signal line on the gate tape carrier packages" and "a second control signal line on the gate tape carrier packages, the second control signal line being connected to the conductive lines to supply a second control signal to the gate driving integrated circuits mounted on the gate tape carrier packages so that the gate lines of the image display part may be sequentially driven from the last one to the first one by applying the second control signal to the last one through a

second signal path having the conductive lines and the second control signal line on the gate tape carrier packages”. None of the cited references, singly or in combination, teach or suggest at least these features of the claimed invention.

In the claimed invention, the first and second control signal lines are disposed on the gate tape carrier packages. However, Lim merely discloses “The liquid crystal display panel 8 is provided with m gate lines G1, G2, . . . , Gm and n data lines D1, D2, . . . , Dn crossing each other. A TFT 5 is formed at each intersection between the m gate lines G1, G2, . . . , Gm and the n data lines D1, D2, . . . , Dn. The TFT 5 is connected to a pixel electrode to drive a liquid crystal pixel cell 6. Tape carrier packages (TCPs) 1 and 4 are attached to pads of the gate lines G1, G2, . . . , Gm, and the data lines D1, D2, . . . , Dn of the liquid crystal display panel 8 respectively. The TCPs 1 and 4 have mounted thereon integrated circuits (IC's) 2 and 3.” See page 1, paragraph 13. That is, in Lim the position of the first and second control signal lines are not shown.

The Examiner acknowledged that Lim does not teach the position of the first and second control signal lines. In rejecting claim 1, the Examiner stated Lim teaches “...a first control signal line formed together with the conductive lines to supply a first control signal to the gate driving integrated circuits so that the gate lines of the image display part may be sequentially driven from the first one to the last one; a second control signal line formed together with the conductive lines to supply a second control signal to the gate driving integrated circuits so that the gate lines of the image display part may be sequentially driven from the last one to the first one...” See Office Action, line 3-16 page 3.

That is, Examiner does not mention the expressions “a first control signal line in the gate tape carrier packages” and “a second control signal line in the gate tape carrier packages”, since Lim does not teach the position of the first and second signal lines.

Further, in the claimed invention the first and second control signals are respectively applied to the gate driving integrated circuit through the first and second paths having conductive lines and the control signal lines. On the contrary, in Lim the path for applying the signal is not shown.

Accordingly, Applicants respectfully submit that claim 1 is allowable over the cited references.

Applicants respectfully traverse the rejection of claims 2 and 4 and reconsideration is respectfully requested. Claims 2 and 4 are allowable at least by virtue of the fact that they depend from claim 1, which discussed above and is allowable over the applied references.

Claim 12 is allowable at least in that the claim recites a combination of elements, including, for example, “supplying a first control signal to the first gate driving integrated circuits through a first signal path having the first control line on the gate tape carrier packages and the conductive lines so that the gate lines of the image display part may be sequentially driven from the first one to the last one” and “supplying a second control signal to the last gate driving integrated circuits through a second signal path having the second control line on the gate tape carrier packages and the conductive lines so that the gate lines of the image display part may be sequentially driven from the last one to the first one.” None of the cited references, singly or in combination, teach or suggest at least these features of the claimed invention.

In the Office Action, the Examiner rejects claim 12 for the same reasons as claim 1. Thus, Applicants’ arguments with respect to claim 1 are equally applicable to claim 12, and Applicants respectfully submit that claim 12 is allowable over the cited references for the same reasons given for claim 1 above.

Applicants respectfully traverse the rejection of claims 16 and 18 and reconsideration is respectfully requested. Claims 16 and 18 are allowable at least by virtue of the fact that they depend from claim 12, which is allowable.

The rejection of claims 6, 8, 13 and 15 under 35 U.S.C. § 103(a) as being unpatentable over Lim in view of Murade and further in view of Aoki is respectfully traversed and reconsideration is requested. Claims 6 and 8 have been cancelled. Thus, the rejection as applied to these cancelled claims is rendered moot.

Applicants respectfully traverse the rejection of claims 13 and 15 and reconsideration is respectfully requested. Claims 13 and 15 are allowable at least by virtue of the fact that they depend from claim 12, which is allowable.

Applicants believe the foregoing amendments and remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: March 19, 2008

Respectfully submitted,

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